

CLAIM LISTING

1. (Original) A method of reducing memory required to decode an audio signal in an audio decoding system, the method comprising:

performing a first audio decoding function;

writing data corresponding to the first audio decoding function to a memory;

performing a second audio decoding function;

writing data corresponding to the second audio decoding function to at least a portion of the memory, whereby the data corresponding to the first audio function is at least partially overwritten by the data corresponding to the second audio decoding function.

2. (Original) The method according to claim 1, wherein the audio decoding functions are for processing all groups together.

3. (Original) The method according to claim 2, further comprising performing the audio decoding functions in an order based upon memory allocation.

4. (Original) The method according to claim 2, wherein a minimum amount of memory to perform the audio decoding functions in a particular group is allocated.

5. (Original) The method according to claim 1, wherein a minimum amount of memory to perform all audio decoding functions in a single memory device is allocated.

6. (Original) The method according to claim 1, wherein each of a plurality of memory devices are allocated to storing data corresponding to at least one audio decoding function.

7. (Original) The method according to claim 6, wherein each of a plurality of memory devices are allocated to storing data corresponding to a group of audio decoding functions.

8. (Original) The method according to claim 1, wherein at least one audio decoding function is optimized to reduce memory used to process the function.

9. (Original) The method according to claim 1, further comprising performing a plurality of audio decoding functions in a single memory device, wherein memory sufficient to perform an audio decoding function using a maximum amount of memory is allocated, and each audio decoding function is performed using less memory in the single memory device.

10. (Original) The method according to claim 1, further comprising:

performing a third audio decoding function to another memory; and

writing data corresponding to the second audio decoding function to at least a portion of the another memory, whereby the data corresponding to the third audio function is at least partially overwritten by the data corresponding to the second audio decoding function.

11. (Original) The method according to claim 1, further comprising:

performing a plurality of audio decoding functions grouped into a plurality of groupings in a plurality of memory devices;

allocating memory sufficient to perform an audio decoding function using a maximum amount of memory within each grouping; and

performing all audio decoding functions in a grouping within the memory allocated to perform the function using the maximum amount of memory.

12. (Original) An integrated circuit for decoding a audio data, the integrated circuit comprising:

a controller;

a memory connected to the controller;

an instruction memory connected to the controller, the instruction memory comprising a plurality of instructions,

wherein execution of the plurality of instructions by the controller causes:

- performing a first audio decoding function;

- writing data corresponding to the first audio decoding function to the memory;

- performing a second audio decoding function; and

- writing data corresponding to the second audio decoding function to at least a portion of the memory, whereby the data corresponding to the first audio function is at least partially overwritten by the data corresponding to the second audio function.

13. (Original) The integrated circuit of claim 12, wherein the audio decoding functions to be processed are grouped together.

14. (Original) The integrated circuit of claim 13, wherein execution of the plurality of instructions by the controller causes performing of the audio decoding functions in an order based upon memory allocation.

15. (Original) The integrated circuit of claim 13, further comprising:

- a plurality of memory devices connected to the controller, wherein each of the plurality of memory devices are allocated to storing data corresponding to at least one audio decoding function.

16. (Original) The integrated circuit of claim 15, wherein each of the plurality of memory devices are allocated to storing data corresponding to a group of audio decoding functions.

17. (Original) The integrated circuit of claim 12, wherein at least one audio decoding function is optimized to reduce memory used to store data corresponding to the at least one audio decoding function.

18. (Original) A system for decoding audio signals with a minimum amount of memory, the system comprising:

a controller for performing a plurality of audio functions; and

at least one memory device for storing data corresponding to an initially performed audio decoding function, and storing data corresponding to a later performed audio decoding function, wherein the data corresponding to the initially performed audio decoding function is at least partially overwritten by the data corresponding to the later performed audio decoding function.

19. (Original) The system of claim 18, further comprising an instruction memory for storing a plurality of instructions, wherein execution of the plurality of instructions by the controller causes performing the plurality of audio functions.